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APPENDIX A

DEVICE FOR CONTROLLING A SETUP/HOLD TIME OF AN INPUT SIGNAL

BACKGROUND OF THE INVENTION

5 1. Field of the Invention

[0001] The present invention generally relates to a device for controlling a setup/hold time of an input signal, and more specifically, to a technique to control the setup/hold time of various control signals applied from an input buffer without physically changing the control device.

2. Description of the Prior Art

[0002] Fig. 1 is a circuit diagram illustrating a conventional device for controlling setup/hold time of input signal.

[0003] The conventional device for controlling a setup/hold time of an input signal comprises inverters IV1~IV4 for performing a driver function, MOS capacitors C1~C4 for performing a signal delay function, metal option unit 2 and 3, and a latch 4.

[0004] Here, the inverters IV1 and IV2 output signals by driving an address, a command signal or input data applied from an input buffer 1. The inverter IV3 outputs a signal by driving an output signal of the metal option unit 2.